

Docket No.: 214391US2S



COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 09/961,248

Applicants: Hidetaka HATTORI

Filing Date: September 25, 2001

For: POWER SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

Group Art Unit: 2814

Examiner: Farahani, D.

SIR:

Attached hereto for filing are the following papers:

RESPONSE

Our check in the amount of **\$0.00** is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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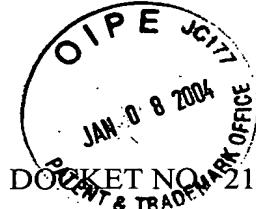
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DOCKET NO. 214391US2S

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

HIDETAKA HATTORI

: EXAMINER: FARAHANI, D.

SERIAL NO: 09/961,248

:

FILED: SEPTEMBER 25, 2001

: GROUP ART UNIT: 2814

FOR: POWER SEMICONDUCTOR
DEVICE AND MANUFACTURING
METHOD THEREOF

:

RESPONSE

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ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated October 16, 2003, Applicants request reconsideration of the above-identified application.

Remarks begin on page 2 of this paper.